## Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

13172

Application ID:

09683243

Title of Invention:

Integrated level shift and override

circuit for a processor system

First Named Inventor:

Chia-Hsing Yu

Domestic/Foreign Application:

**Domestic Application** 

Filing Date:

null

**Effective Receipt Date:** 

2001-12-05

**Submission Type:** 

**Utility Patent Filing** 

Filing Type:

new-utility

**Confirmation Number:** 

0

**Attorney Docket Number:** 

VIAPO010USA

Digital Certificate Holder:

cn=Winston Hsu, ou=Registered Attorneys, ou=Patent and Trademark Office, ou=Department of Commerce, o=U.S.

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Certificate Message Digest:

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Total Fees Authorized:

\$780.0

Payment Category:

**DA** – Deposit Account

**Deposit Account Number:** 

500801

Deposit Account Name:

**WINSTON HSU** 





Electronic Version 1.0.2

Stylesheet Version: 1.0

Attorney Docket Number: VIAP0010USA

# Integrated level shift and override circuit for a processor system

First Named Inventor: Chia-Hsing Yu

SUBMITTED BY

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41,526

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Date Signed: 20011205

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### Attached Files:

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specification

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declaration

declaration

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### **TOTAL FEES AUTHORIZED: \$ 780**

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VAEB-JMXX-8IIL

Date Signed:

20011205

### **BASIC FILING FEE**

Fee Description	Fee Code	Fee Paid
Utility Filing Fee	101	\$ 740

Subtotal For Basic Filing Fee: \$ 740

### **EXTRA CLAIM FEES**

	Fee Code	Fee	Extra Claims	Fee Paid
Total Claims: 15	103	\$ 18	0	\$ 0
Independent Claims: 1	102	\$ 84	0	\$ 0

Subtotal For Extra Claims Fees: \$ 0

### **ADDITIONAL FEES**

Fee Description	Fee Code	Fee Paid
Recording Each Patent Assignment Per Property Fee	581	\$ 40

Subtotal For Additional Fees: \$ 40